

The diagram illustrates the system architecture of the MST51510. It is divided into three main functional areas:

- Input/Output Interfaces (Left):**
  - PC (Analog):** Connected to the CVBS IN pin of the AV1 connector.
  - PC (Digital):** Connected to the RGB TN pin of the AV1 connector.
  - DTV (COMP):** Connected to the S-video video pin of the AV3 connector.
  - PC Audio:** Connected to the AUDIO IN pin of the AV1 connector.
  - SCART(Ext.):** Connected to the CVBS IN, RGB TN, AUDIO IN, CVBS OUT, and AUDIO OUT pins of the AV1 connector.
  - Video(AV):** Connected to the S Video, VIDEO, AUDIO L, and AUDIO R pins of the AV3 connector.
  - FMRADIO Tuner:** Connected to the SAW filter and the FMRADIO pin of the AV3 connector.
- Internal Processing Blocks (Center):**
  - AV1 and AV3 Connectors:** Multi-pin connectors for external inputs and outputs.
  - Internal Blocks:** ITU656, DDP, MCU (SDA5550), DRX, VSP, MSP, and VCTI.
- Peripheral Components (Right):**
  - Audio AMP:** Receives signals from the internal blocks and drives the speakers.
  - HPAMP:** Receives signals from the internal blocks and drives the headphones.
  - KeyPad/IR:** User input device.
  - ROM (512kB):** Non-volatile memory.
  - SAW Filter:** Surface Acoustic Wave filter for the radio tuner.

## Memo